

GP 1758

Patent

Case No.: 57322US0026

THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: BAUDE, PAUL F.

Application No.: 10/076174

Group Art Unit: 1756

Filed: February 14, 2002

Examiner: Unknown

Title: APERTURE MASKS FOR CIRCUIT FABRICATION

RECEIVED
MAY 21 2002
TC 1700INFORMATION DISCLOSURE STATEMENTCommissioner for Patents
Washington, DC 20231CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on:

MAY 14 2002

Date

Signed by: Lydia M. Cahill

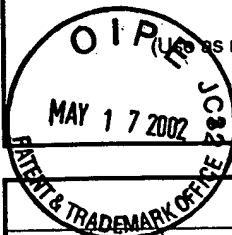
Dear Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, enclosed please find a completed PTO-1449 form citing references submitted for consideration during examination of the above-referenced patent application. A copy of each of the references cited therein is also enclosed.

It is respectfully requested that the Examiner initial and return the enclosed PTO-1449 form in order to indicate that each of the references listed thereon has been considered in connection with the present application.

Respectfully submitted,

May 14, 2002
DateBy:
Lucy C. Weiss, Reg. No.: 32,834
Telephone No.: 651-733-1189Office of Intellectual Property Counsel
3M Innovative Properties Company
P.O. Box 33427
St. Paul, MN 55133-3427
Facsimile No.: 651-736-3833RECEIVED
JUN - 7 2002
TECHNOLOGY CENTER 2800

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Page 2 of 2

Application Number	10/076174
Filing Date	February 14, 2002
First Named Inventor	Baude, Paul F.
Art Unit	1756
Examiner Name	
Attorney Case Number	57322US002

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

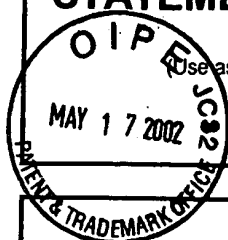
Exam. Init.*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	C4	Schon et al., "Fast Organic Electronic Circuits Based on Ambipolar Pentacene Field-Effect Transistors," Applied Physics Letters, Vol. 79, No. 24, pp. 4043-4044 (2001),
	C5	Gouy et al., "Deposition of Organic Semiconductor Through Silicon Shadow Masks for Integrated Led on Silicon."
	C6	Kim et al., "Photoplastic Shadow-Masks for Rapid Resistless Multi-Layer Micropatterning".
	C7	Kim et al., "Dry-Lift-Off Patterning on Fragile Surfaces Using MEMS Shadow Masks," pp. 619-622.

*Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT



Use as many sheets as necessary)

Page 1 of 2

Application Number	10/076174
Filing Date	February 14, 2002
First Named Inventor	Baude, Paul F.
Art Unit	1756
Examiner Name	
Attorney Case Number	57322US002

U.S. Patent Documents

Exam. Init.*	Cite No.	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Doc. Number-(Kind Code if Known)			
	A1	US- 3,669,060	06/13/72	Page et al.	RECEIVED MAY 21 2002 TC 1700
	A2	US- 4,096,821	06/27/78	Greeneich et al.	
	A3	US- 4,335,161	06/15/82	Luo	
	A4	US- 4,676,193	06/30/87	Martin	
	A5	US- 4,746,548	05/24/88	Boudreau et al.	
	A6	US- 4,915,057	04/10/90	Boudreau et al.	
	A7	US- 4,963,921	10/16/90	Kariya et al.	
	A8	US- 5,397,607	03/14/95	Shields et al.	
	A9	US- 6,087,196	07/11/00	Sturm et al.	
	A10	US- 6,106,627	08/22/00	Yializis	
	A11	US- 6,285,001 B1	09/04/01	Fleming et al.	

RECEIVED
MAY 21 2002
TC 1700

Foreign Patent Documents

Exam. Init.*	Cite No.	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation (Check if yes)
		Ctry. Code	Number-KindCode (if known)				
	B1	EP	0 118 576	12/02/87			
	B2	EP	0719 638 A2	07/03/96			
	B3	JP	5-228669	09/07/93			X
	B4	JP	9-27454	01/28/97			X
	B5	PCT	WO 99/30336	06/17/99			
	B6	PCT	WO 99/39373	08/05/99			
	B7	PCT	WO 99/54786	10/28/99			

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Exam. Init.*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	C1	Brody, "Large Area Masking Techniques for Thin Film Transistor Arrays," SPIE, Vol. 100, Semiconductor Microlithography II, pp. 140-150(1977)
	C2	Weimer, "The TFT- A New Thin-Film Transistor," Proceedings of the IRE, pp. 1462-1469(1962).
	C3	Brody et al., "Flexible Thin-Film Transistors Stretch Performance, Shrink Cost," Research, pp. 20-23 (1969).

*Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.